

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Lap-Wai CHOW, et al. )  
Serial No.: not yet assigned ) Group: Not yet assigned  
Filed: Concurrently herewith ) Examiner: Not yet assigned  
For: "INTEGRATED CIRCUIT ) Our Ref: B-4425NP 621267-1  
MODIFICATION USING )  
WELL IMPLANTS" ) Date: December 12, 2003

CLAIM TO PRIORITY UNDER 35 U.S.C. 119(e)

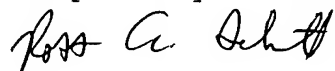
MAIL STOP PATENT APPLICATION  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

[X] Applicants hereby make a right of priority claim under 35  
U.S.C. 119(e) for the benefit of the filing date(s) of the  
following corresponding provisional application(s)

<u>COUNTRY</u>	<u>FILING DATE</u>	<u>PROVISIONAL APPLICATION NO.</u>
U.S.A.	December 13, 2002	60/433,314

Respectfully submitted,



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